

Notice of References Cited

Application No.
09/296,835

Applicant(s)
Weimer et al.

Examiner
Erik Kielin

Group Art Unit
2813

Page 1 of 1

U.S. PATENT DOCUMENTS

	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS
A	5,534,716	7/1996	Takemura	257	72
B	5,661,072	8/1997	Jeng	438	439
C	5,728,603	3/1998	Emesh et al.	438	3
D	6,114,258	9/2000	Miner et al.	438	787
E					
F					
G					
H					
I					
J					
K					
L					
M					

FOREIGN PATENT DOCUMENTS

	DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUBCLASS
N						
O						
P						
Q						
R						
S						
T						

NON-PATENT DOCUMENTS

	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
U	Wolf, et al., Silicon Processing for the VLSI Era, Vol. 1: Process Technology, Lattice Press: Sunset Beach CA, p.57.	1986
V		
W		
X		